

## STGF30NC60S STGP30NC60S

30 A - 600 V - fast IGBT

### **Features**

- Very low on-voltage drop (V<sub>CE(sat)</sub>)
- High current capability
- Minimum power losses at 5 kHz in hard switching
- Optimized performance for medium operating frequencies.

### **Application**

■ Medium frequency motor control

### **Description**

This IGBT utilizes the advanced PowerMESH™ process resulting in an excellent trade-off between switching performance and low on-state behavior.

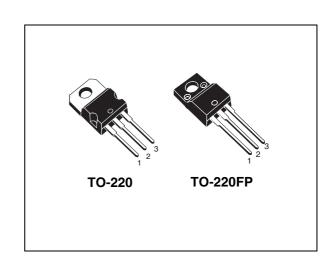


Figure 1. Internal schematic diagram

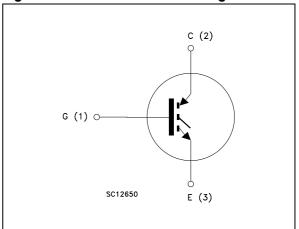


Table 1. Device summary

Part number	Marking	Package	Packaging
STGF30NC60S	GF30NC60S	TO-220FP	Tube
STGP30NC60S	GP30NC60S	TO-220	Tube

## **Contents**

1	Electrical ratings
2	Electrical characteristics
	2.1 Electrical characteristics (curves)
3	Test circuits
4	Package mechanical data
5	Revision history

## 1 Electrical ratings

Table 2. Absolute maximum ratings

Cumbal	Parameter	Va	Linit	
Symbol	Parameter	TO-220	TO-220FP	Unit
V <sub>CES</sub>	Collector-emitter voltage (V <sub>GE</sub> = 0)	60	00	V
I <sub>C</sub> <sup>(1)</sup>	Continuous collector current at T <sub>C</sub> = 25 °C	55	22	Α
I <sub>C</sub> <sup>(1)</sup>	Continuous collector current at T <sub>C</sub> = 100 °C	35	11	Α
I <sub>CL</sub> <sup>(2)</sup>	Turn-off latching current	150		Α
I <sub>CP</sub> <sup>(3)</sup>	Pulsed collector current	150		Α
$V_{GE}$	Gate-emitter voltage	±20		٧
V <sub>ISO</sub>	Insulation withstand voltage (RMS) from all three leads to external heat sink (t=1 s; $T_C$ =25 °C)		2500	٧
P <sub>TOT</sub>	Total dissipation at T <sub>C</sub> = 25 °C	175	40	W
T <sub>j</sub>	Operating junction temperature	– 55 t	o 150	°C

1. Calculated according to the iterative formula:

$$I_{C}(T_{C}) = \frac{T_{j(max)} - T_{C}}{R_{thj-c} \times V_{CE(sat)(max)}(T_{j(max)}, I_{C}(T_{C}))}$$

- 2.  $V_{clamp}$  = 80%,( $V_{CES}$ ),  $T_j$  =150 °C,  $R_G$  = 10  $\Omega$ ,  $V_{GE}$  = 15 V
- 3. Pulse width limited by max. junction temperature allowed

Table 3. Thermal data

Symbol Parameter		Val	lue	Unit
Symbol	r al allietei	TO-220	TO-220FP	
R <sub>thj-case</sub>	Thermal resistance junction-case	0.7	3.1	°C/W
R <sub>thj-amb</sub>	Thermal resistance junction-ambient 62.5		2.5	°C/W

## 2 Electrical characteristics

(T<sub>J</sub>= 25 °C unless otherwise specified)

Table 4. Static

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)CES</sub>	Collector-emitter breakdown voltage (V <sub>GE</sub> = 0)	I <sub>C</sub> = 1 mA	600			V
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 20 A V <sub>GE</sub> = 15 V, I <sub>C</sub> = 20 A,T <sub>J</sub> =150 °C		1.5 1.4	1.9	V V
V <sub>GE(th)</sub>	Gate threshold voltage	V <sub>CE</sub> = V <sub>GE</sub> , I <sub>C</sub> = 250 μA	3.75		5.75	٧
I <sub>CES</sub>	Collector cut-off current (V <sub>GE</sub> = 0)	V <sub>CE</sub> = 600 V V <sub>CE</sub> = 600 V, T <sub>J</sub> = 150 °C			150 1	μA mA
I <sub>GES</sub>	Gate-emitter cut-off current (V <sub>CE</sub> = 0)	V <sub>GE</sub> = ±20 V			±100	nA
9 <sub>fs</sub> <sup>(1)</sup>	Forward transconductance	V <sub>CE</sub> = 15 V <sub>,</sub> I <sub>C</sub> = 20 A		10		S

<sup>1.</sup> Pulsed: pulse duration = 300 μs, duty cycle 1.5%

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C <sub>ies</sub> C <sub>oes</sub> C <sub>res</sub>	Input capacitance Output capacitance Reverse transfer capacitance	V <sub>CE</sub> = 25 V, f = 1 MHz, V <sub>GE</sub> =0	-	2200 185 48.5	-	pF pF pF
Q <sub>g</sub> Q <sub>ge</sub> Q <sub>gc</sub>	Total gate charge Gate-emitter charge Gate-collector charge	$V_{CE} = 480 \text{ V, } I_{C} = 20 \text{ A,}$ $V_{GE} = 15 \text{ V}$ Figure 18	-	96 14 44.5	-	nC nC nC

Table 6. Switching on/off (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t <sub>d(on)</sub> t <sub>r</sub> (di/dt) <sub>on</sub>	Turn-on delay time Current rise time Turn-on current slope	$V_{CC} = 480 \text{ V}, I_{C} = 20 \text{ A}$ $R_{G} = 10 \Omega, V_{GE} = 15 \text{ V}$ Figure 17	-	21.5 8.5 2280	-	ns ns A/µs
t <sub>d(on)</sub> t <sub>r</sub> (di/dt) <sub>on</sub>	Turn-on delay time Current rise time Turn-on current slope	$V_{CC} = 480 \text{ V}, I_{C} = 20 \text{ A}$ $R_{G} = 10 \Omega, V_{GE} = 15 \text{ V},$ $T_{J} = 125 ^{\circ}\text{C}$ Figure 17	-	20.5 9.5 2150	-	ns ns A/µs
$t_r(V_{off})$ $t_d(_{off})$ $t_f$	Off voltage rise time Turn-off delay time Current fall time	$V_{cc}$ = 480 V, $I_{C}$ = 20 A, $R_{GE}$ = 10 $\Omega$ , $V_{GE}$ = 15 V Figure 17	-	85 180 200	-	ns ns ns
t <sub>r</sub> (V <sub>off</sub> ) t <sub>d</sub> ( <sub>off</sub> ) t <sub>f</sub>	Off voltage rise time Turn-off delay time Current fall time	$V_{cc} = 480 \text{ V}, I_{C} = 20 \text{ A},$ $R_{GE} = 10 \Omega, V_{GE} = 15 \text{ V}$ $T_{J} = 125 ^{\circ}\text{C}$ Figure 17	-	155 260 295	-	ns ns ns

Table 7. Switching energy (inductive load)

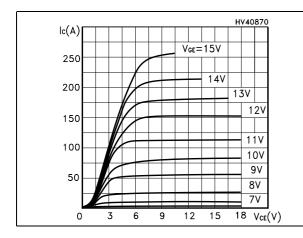
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Eon E <sub>off</sub> <sup>(1)</sup> E <sub>ts</sub>	Turn-on switching losses Turn-off switching losses Total switching losses	$V_{CC}$ = 480 V, $I_{C}$ = 20 A $R_{G}$ = 10 $\Omega$ , $V_{GE}$ = 15 V, Figure 17	-	300 1275 1575	-	μJ μJ μJ
Eon E <sub>off</sub> <sup>(1)</sup> E <sub>ts</sub>	Turn-on switching losses Turn-off switching losses Total switching losses	$V_{CC} = 480 \text{ V}, I_{C} = 20 \text{ A}$ $R_{G} = 10 \Omega, V_{GE} = 15 \text{ V},$ $T_{J} = 125 ^{\circ}\text{C}$ Figure 17	-	430 1965 2395	-	μJ μJ μJ

<sup>1.</sup> Turn-off losses include also the tail of the collector current.

## 2.1 Electrical characteristics (curves)

Figure 2. Output characteristics

Figure 3. Transfer characteristics



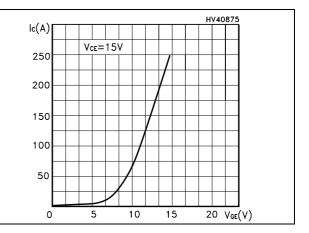
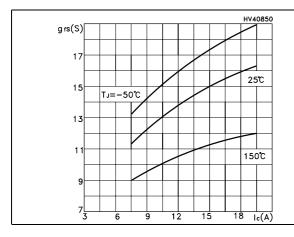


Figure 4. Transconductance

Figure 5. Collector-emitter on voltage vs temperature



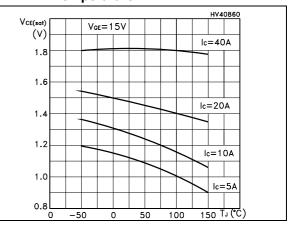
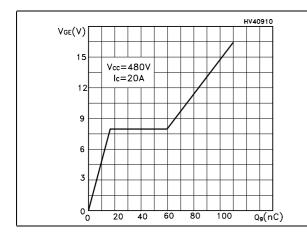


Figure 6. Gate charge vs gate-source voltage Figure 7. Capacitance variations



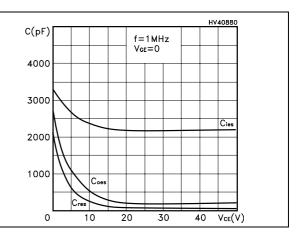


Figure 8. Normalized gate threshold voltage Figure 9. Collector-emitter on voltage vs vs temperature collector current

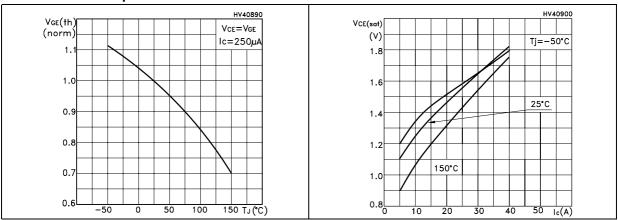


Figure 10. Normalized breakdown voltage vs Figure 11. Switching losses vs temperature temperature

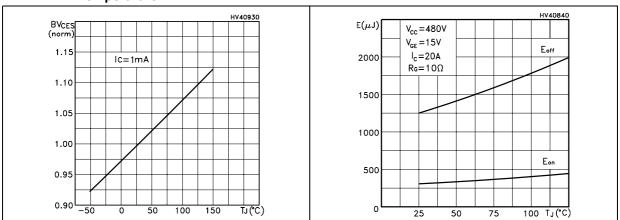


Figure 12. Switching losses vs gate resistance Figure 13. Switching losses vs collector current

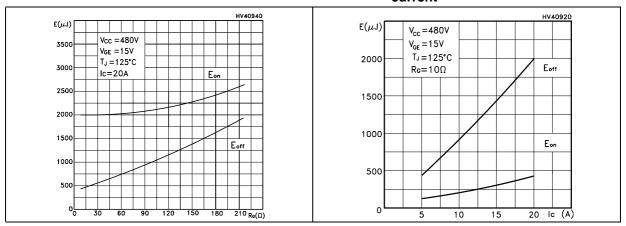


Figure 14. Thermal Impedance

Figure 15. Turn-off SOA

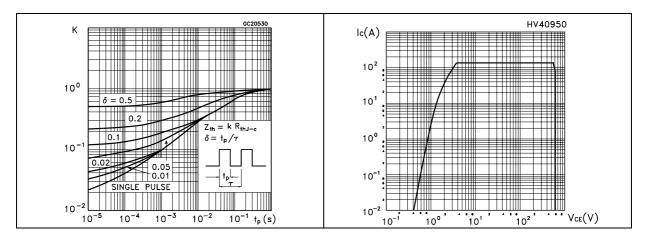
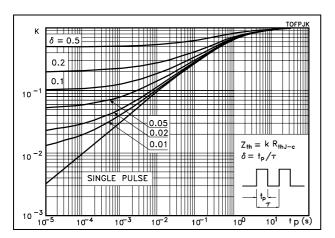


Figure 16. Thermal Impedance for TO-220FP



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## 3 Test circuits

Figure 17. Test circuit for inductive load switching

Figure 18. Gate charge test circuit

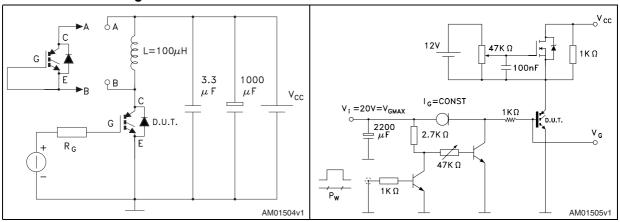
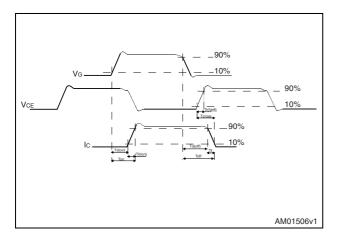


Figure 19. Switching waveforms



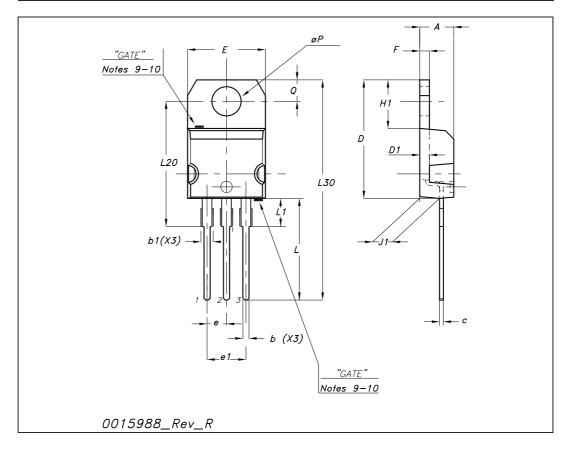
## 4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

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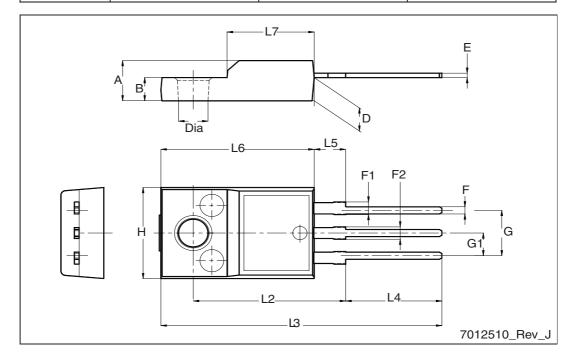
### TO-220 mechanical data

Dim		mm			inch	
Dim	Min	Тур	Max	Min	Тур	Max
A	4.40		4.60	0.173		0.181
b	0.61		0.88	0.024		0.034
b1	1.14		1.70	0.044		0.066
С	0.48		0.70	0.019		0.027
D	15.25		15.75	0.6		0.62
D1		1.27			0.050	
E	10		10.40	0.393		0.409
е	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
F	1.23		1.32	0.048		0.051
H1	6.20		6.60	0.244		0.256
J1	2.40		2.72	0.094		0.107
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L20		16.40			0.645	
L30		28.90			1.137	
ØP	3.75		3.85	0.147		0.151
Q	2.65		2.95	0.104		0.116



### **TO-220FP mechanical data**

Dim		mm				
Dim.	Min.	Тур.	Max.			
А	4.4		4.6			
В	2.5		2.7			
D	2.5		2.75			
E	0.45		0.7			
F	0.75		1			
F1	1.15		1.70			
F2	1.15		1.5			
G	4.95		5.2			
G1	2.4		2.7			
Н	10		10.4			
L2		16				
L3	28.6		30.6			
L4	9.8		10.6			
L5	2.9		3.6			
L6	15.9		16.4			
L7	9		9.3			
Dia	3		3.2			



# 5 Revision history

Table 8. Document revision history

Date	Revision	Changes
02-Jul-2007	1	Initial release
20-Nov-2007	2	Document status promoted from preliminary data to datasheet
04-May-2009	3	Added new package, mechanical data: TO-220FP

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